

2.5V Drive Nch+Pch MOSFET

QS6M3

●Structure

Silicon N-channel / P-channel MOSFET

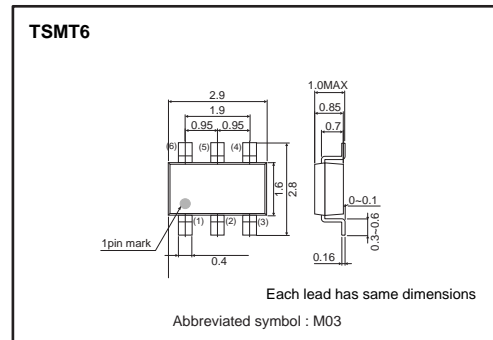
●Features

- 1) Low on-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (TSMT6).

●Application

Power switching, DC / DC converter.

●Dimensions (Unit : mm)



●Packaging specifications

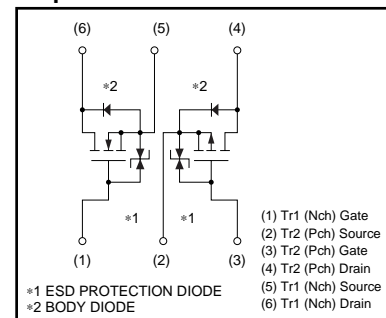
Type	Package	Taping
	Code	TR
	Basic ordering unit (pieces)	3000
QS6M3		○

●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits		Unit
		Tr1 : Nch	Tr2 : Pch	
Drain-source voltage	V _{DSS}	30	-20	V
Gate-source voltage	V _{GSS}	±12	±12	V
Drain current	Continuous	I _D	±1.5	A
	Pulsed	I _{DP} *1	±6.0	A
Source current (Body diode)	Continuous	I _S	0.8	A
	Pulsed	I _{SP} *1	6.0	A
Total power dissipation	P _D *2		1.25	W / TOTAL
			0.9	W / ELEMENT
Channel temperature	T _{ch}	150		°C
Storage temperature	T _{stg}	-55 to +150		°C

*1 P_w≤10μs, Duty cycle≤1%
*2 Mounted on a ceramic board

●Equivalent circuit



●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	R _{th} (ch-a)*	100	°C / W / TOTAL
		139	°C / W / ELEMENT

* Mounted on a ceramic board

Transistors

N-ch

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I _{gss}	–	–	±10	μA	V _{GS} =±12V, V _{DS} =0V
Drain-source breakdown voltage	V _{(BR) DSS}	30	–	–	V	I _D =1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	–	–	1	μA	V _{DS} =30V, V _{GS} =0V
Gate threshold voltage	V _{GS(th)}	0.5	–	1.5	V	V _{DS} =10V, I _D =1mA
Static drain-source on-state resistance	R _{DS(on)} *	–	170	230	mΩ	I _D =1.5A, V _{GS} =4.5V
		–	180	245		I _D =1.5A, V _{GS} =4.0V
		–	260	360		I _D =1.0A, V _{GS} =2.5V
Forward transfer admittance	Y _{fs} *	1.0	–	–	S	I _D =1.0A, V _{DS} =10V
Input capacitance	C _{iss}	–	80	–	pF	V _{DS} =10V
Output capacitance	C _{oss}	–	25	–	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	–	15	–	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	–	7	–	ns	I _D =1A, V _{DD} ≐15V
Rise time	t _r *	–	18	–	ns	V _{GS} =4.5V
Turn-off delay time	t _{d(off)} *	–	15	–	ns	R _L =15Ω
Fall time	t _f *	–	15	–	ns	R _G =10Ω
Total gate charge	Q _g *	–	1.6	–	nC	V _{DD} ≐15V R _L =10Ω
Gate-source charge	Q _{gs} *	–	0.5	–	nC	V _{GS} =4.5V R _G =10Ω
Gate-drain charge	Q _{gd} *	–	0.9	–	nC	I _D =1.5A

*Pulsed

●Body diode characteristics (Source-Drain) (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V _{SD} *	–	–	1.2	V	I _S =3.2A, V _{GS} =0V

*Pulsed

Transistors

P-ch

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I_{GSS}	-	-	±10	μA	$V_{GS} = \pm 12V, V_{DS} = 0V$
Drain-source breakdown voltage	$V_{(BR) DSS}$	-20	-	-	V	$I_D = -1mA, V_{GS} = 0V$
Zero gate voltage drain current	I_{DSS}	-	-	-1	μA	$V_{DS} = -20V, V_{GS} = 0V$
Gate threshold voltage	$V_{GS(th)}$	-0.7	-	-2.0	V	$V_{DS} = -10V, I_D = 1mA$
Static drain-source on-state resistance	$R_{DS(on)}$ *	-	155	215	mΩ	$I_D = -1.5A, V_{GS} = -4.5V$
		-	170	235		$I_D = -1.5A, V_{GS} = -4.0V$
		-	310	430		$I_D = -0.75A, V_{GS} = -2.5V$
Forward transfer admittance	$ Y_{fs} $ *	1.0	-	-	S	$I_D = -0.75A, V_{DS} = -10V$
Input capacitance	C_{iss}	-	270	-	pF	$V_{DS} = -10V$
Output capacitance	C_{oss}	-	40	-	pF	$V_{GS} = 0V$
Reverse transfer capacitance	C_{rss}	-	35	-	pF	$f = 1MHz$
Turn-on delay time	$t_{d(on)}$ *	-	10	-	ns	$I_D = -0.75A, V_{DD} = -15V$
Rise time	t_r *	-	12	-	ns	$V_{GS} = -4.5V$
Turn-off delay time	$t_{d(off)}$ *	-	45	-	ns	$R_L = 20\Omega$
Fall time	t_f *	-	20	-	ns	$R_G = 10\Omega$
Total gate charge	Q_g *	-	3.0	-	nC	$V_{DD} = -15V, R_L = 10\Omega$
Gate-source charge	Q_{gs} *	-	0.8	-	nC	$V_{GS} = -4.5V, R_G = 10\Omega$
Gate-drain charge	Q_{gd} *	-	0.85	-	nC	$I_D = -1.5A$

*Pulsed

●Body diode characteristics (Source-Drain) (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V_{SD}	-	-	-1.2	V	$I_S = -0.75A, V_{GS} = 0V$

Transistors

N-ch

●Electrical characteristic curves

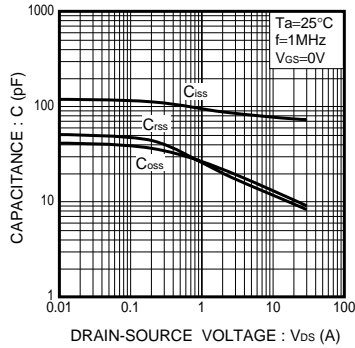


Fig.1 Typical Capacitance vs. Drain-Source Voltage

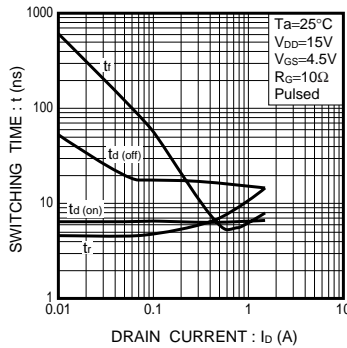


Fig.2 Switching Characteristics

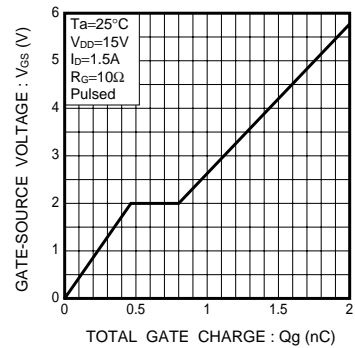


Fig.3 Dynamic Input Characteristics

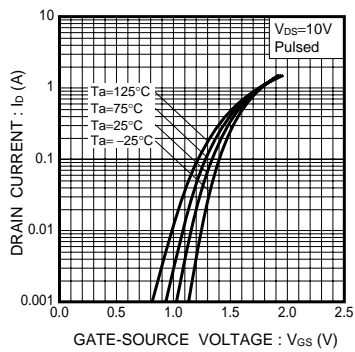


Fig.4 Typical Transfer Characteristics

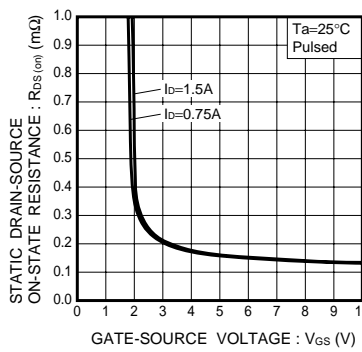


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

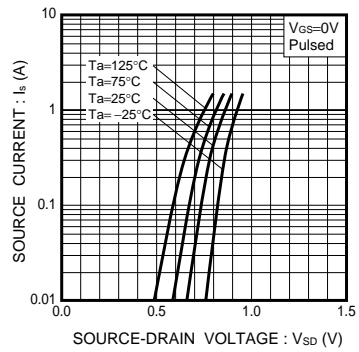


Fig.6 Source Current vs. Source-Drain Voltage

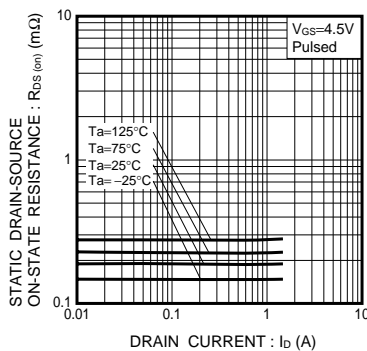


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (I)

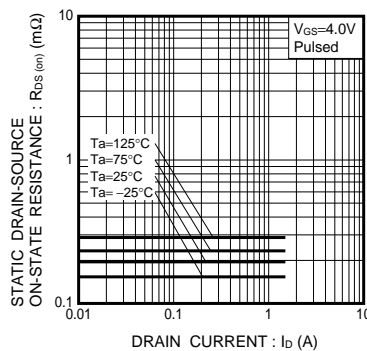


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current (II)

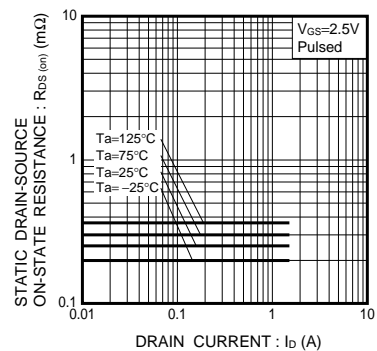


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current (III)

Transistors

P-ch

●Electrical characteristic curves

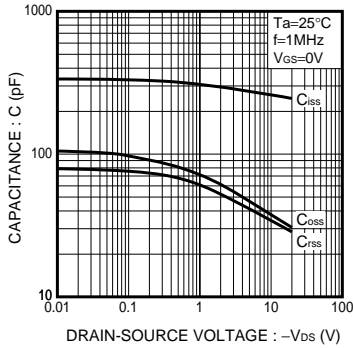


Fig.1 Typical Capacitance vs. Drain-Source Voltage

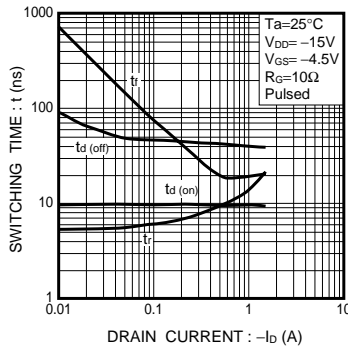


Fig.2 Switching Characteristics

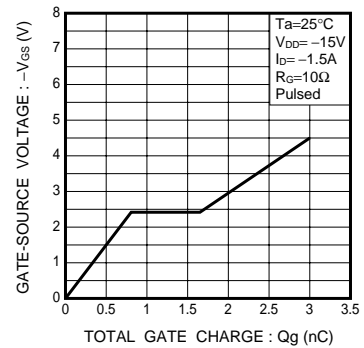


Fig.3 Dynamic Input Characteristics

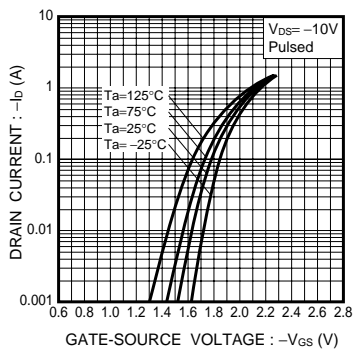


Fig.4 Typical Transfer Characteristics

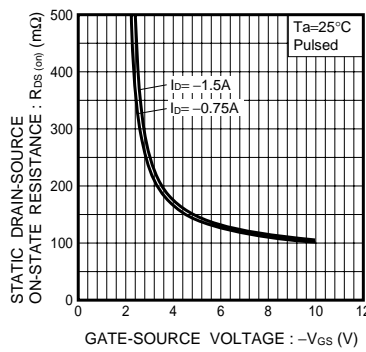


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

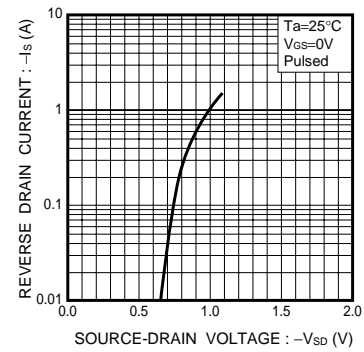


Fig.6 Source Current vs. Source-Drain Voltage

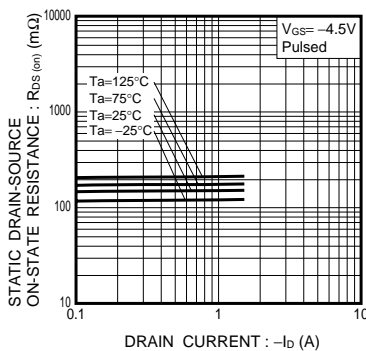


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (I)

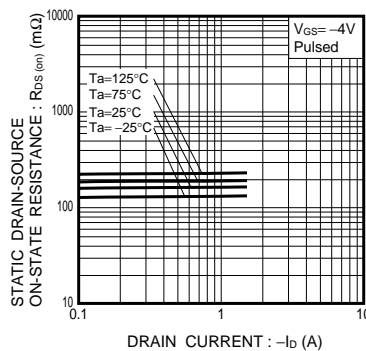


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current (II)

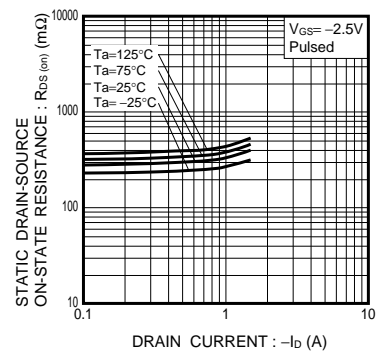


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current (III)

Transistors

N-ch

● Measurement circuit

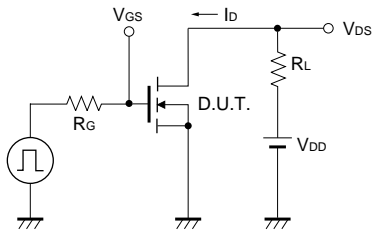


Fig.1-1 Switching Time Measurement Circuit

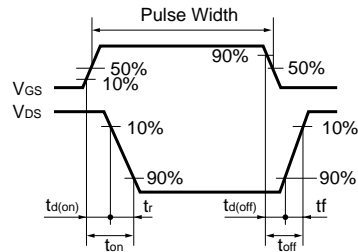


Fig.1-2 Switching Waveforms

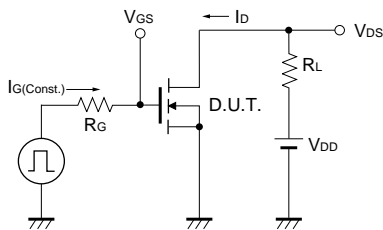


Fig.2-1 Gate Charge Measurement Circuit

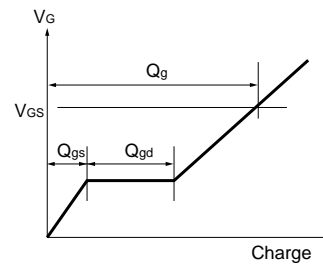


Fig.2-2 Gate Charge Waveform

Transistors

P-ch

●Measurement circuit

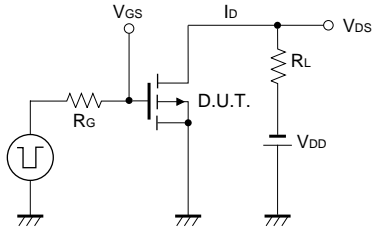


Fig.3-1 Switching Time Measurement Circuit

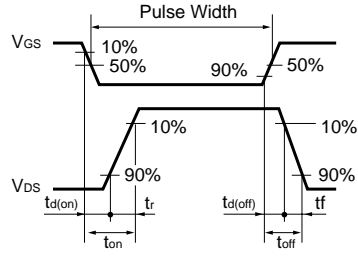


Fig.3-2 Switching Waveforms

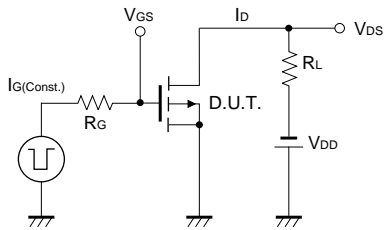


Fig.4-1 Gate Charge Measurement Circuit

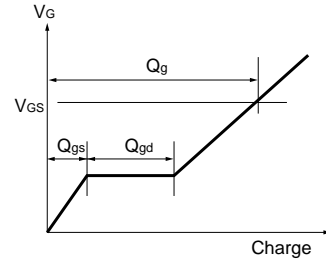


Fig.4-2 Gate Charge Waveform

Notes

- No technical content pages of this document may be reproduced in any form or transmitted by any means without prior permission of ROHM CO.,LTD.
- The contents described herein are subject to change without notice. The specifications for the product described in this document are for reference only. Upon actual use, therefore, please request that specifications to be separately delivered.
- Application circuit diagrams and circuit constants contained herein are shown as examples of standard use and operation. Please pay careful attention to the peripheral conditions when designing circuits and deciding upon circuit constants in the set.
- Any data, including, but not limited to application circuit diagrams information, described herein are intended only as illustrations of such devices and not as the specifications for such devices. ROHM CO.,LTD. disclaims any warranty that any use of such devices shall be free from infringement of any third party's intellectual property rights or other proprietary rights, and further, assumes no liability of whatsoever nature in the event of any such infringement, or arising from or connected with or related to the use of such devices.
- Upon the sale of any such devices, other than for buyer's right to use such devices itself, resell or otherwise dispose of the same, no express or implied right or license to practice or commercially exploit any intellectual property rights or other proprietary rights owned or controlled by
- ROHM CO., LTD. is granted to any such buyer.
- Products listed in this document are no antiradiation design.

The products listed in this document are designed to be used with ordinary electronic equipment or devices (such as audio visual equipment, office-automation equipment, communications devices, electrical appliances and electronic toys).

Should you intend to use these products with equipment or devices which require an extremely high level of reliability and the malfunction of which would directly endanger human life (such as medical instruments, transportation equipment, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), please be sure to consult with our sales representative in advance.

It is our top priority to supply products with the utmost quality and reliability. However, there is always a chance of failure due to unexpected factors. Therefore, please take into account the derating characteristics and allow for sufficient safety features, such as extra margin, anti-flammability, and fail-safe measures when designing in order to prevent possible accidents that may result in bodily harm or fire caused by component failure. ROHM cannot be held responsible for any damages arising from the use of the products under conditions out of the range of the specifications or due to non-compliance with the NOTES specified in this catalog.

Thank you for your accessing to ROHM product informations.

More detail product informations and catalogs are available, please contact your nearest sales office.

ROHM Customer Support System

THE AMERICAS / EUROPE / ASIA / JAPAN

www.rohm.com

Contact us : webmaster@rohm.co.jp